

N-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	Q _g (Typ.)			
20	0.002 at V _{GS} = 10 V	46	34 nC			
20	0.0025 at $V_{GS} = 4.5 \text{ V}$	41	34 110			

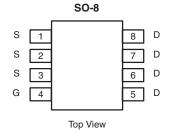
FEATURES

- Halogen-free According to IEC 61249-2-21
- TrenchFET® Power MOSFET
- 100 % R_g and UIS Tested

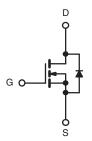
ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

- OR-ing
- DC/DC



Ordering Information: Si4136DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V_{DS}	20	V		
Gate-Source Voltage		V_{GS}	± 20	v	
	T _C = 25 °C		46		
Continuous Drain Current (T _{.I} = 150 °C)	T _C = 70 °C	I _D	37		
Continuous Diam Current (1) = 130 G)	T _A = 25 °C	'D	31 ^{b, c}		
	T _A = 70 °C	1	24.7 ^{b, c}	^	
Pulsed Drain Current		I _{DM}	70	Α	
Continuous Source-Drain Diode Current	T _C = 25 °C		7		
Continuous Source-Drain Diode Current	T _A = 25 °C	Is	3.1 ^{b, c}		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	30		
Avalanche Energy		E _{AS}	45	mJ	
	T _C = 25 °C	P _D	7.8		
Maximum Dawar Dissination	T _C = 70 °C		5	W	
Maximum Power Dissipation	T _A = 25 °C		3.5 ^{b, c}	VV	
	T _A = 70 °C	1	2.2 ^{b, c}		
Operating Junction and Storage Temperature	T _J , T _{stq}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R_{thJA}	29	35	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	13	16	O/ VV		

Notes:

- a. Based on T_C = 25 °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. Maximum under Steady State conditions is 80 °C/W.

Si4136DY

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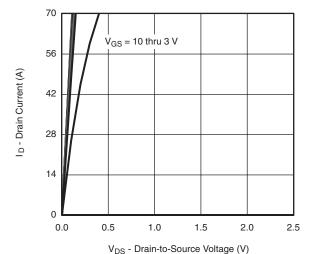
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	-						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		19		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 6			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1.0		2.2	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zawa Cata Walkawa Dwain Cowwood	I _{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ	
Zero Gate Voltage Drain Current		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
	Б	V _{GS} = 10 V, I _D = 15 A		0.00155	0.002	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$		0.00195	0.0025		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 15 A		85		S	
Dynamic ^b				l.			
Input Capacitance	C _{iss}			4560		pF	
Output Capacitance	C _{oss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1285			
Reverse Transfer Capacitance	C _{rss}			545			
·		$V_{DS} = 10 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		73	110	nC	
Total Gate Charge	Q _g			34	50		
Gate-Source Charge	Q_{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 20 \text{ A}$		11			
Gate-Drain Charge	Q_{gd}			9			
Gate Resistance	R_{g}	f = 1 MHz	0.3	1.5	3	Ω	
Turn-On Delay Time	t _{d(on)}			34	60		
Rise Time	t _r	V_{DD} = 10 V, R_L = 1 Ω		26	45	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ 10 A, V_{GEN} = 4.5 V, R_g = 1 Ω		50	90		
Fall Time	t _f			23	40		
Turn-On Delay Time	t _{d(on)}			13	25		
Rise Time	t _r	$V_{DD} = 10 \text{ V}, R_L = 1 \Omega$		11	22		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		43	70		
Fall Time	t _f			9	18		
Drain-Source Body Diode Characterist	cs						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			7	Α	
Pulse Diode Forward Current ^a	I _{SM}				70		
Body Diode Voltage	V_{SD}	I _S = 2 A		0.69	1.1	V	
Body Diode Reverse Recovery Time	t _{rr}			31	47	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 10 A, dl/dt = 100 A/μs, T _{.I} = 25 °C		24	36	nC	
Reverse Recovery Fall Time	ta	i _F = 10 A, αί/αι = 100 Α/μ5, 1 _J = 25 °C		15.5			
Reverse Recovery Rise Time	t _b			15.5		ns	

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing.

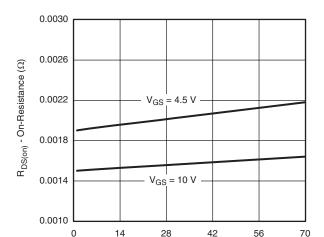
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

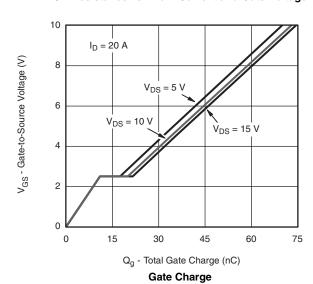


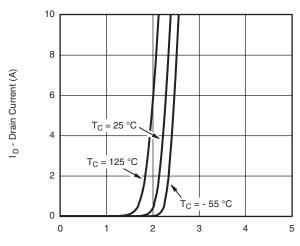
Output Characteristics



I_D - Drain Current (A)

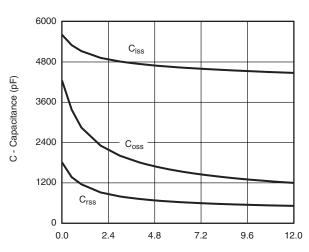
On-Resistance vs. Drain Current and Gate Voltage





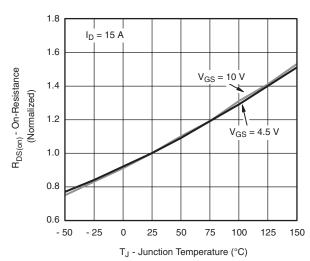
 V_{GS} - Gate-to-Source Voltage (V)

Transfer Characteristics



V_{DS} - Drain-to-Source Voltage (V)

Capacitance



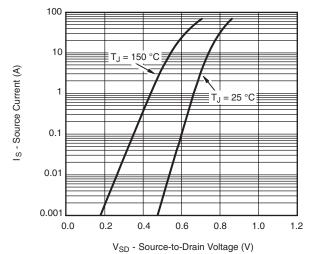
On-Resistance vs. Junction Temperature

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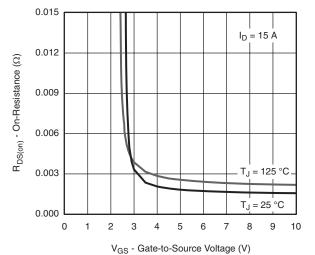
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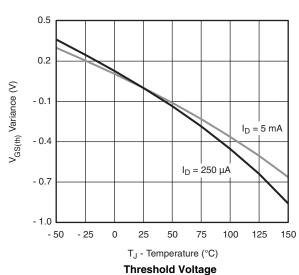
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

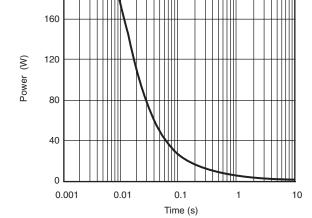


Source-Drain Diode Forward Voltage



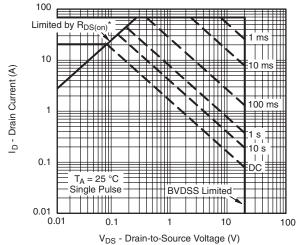
On-Resistance vs. Gate-to-Source Voltage





200

Single Pulse Power, Junction-to-Ambient

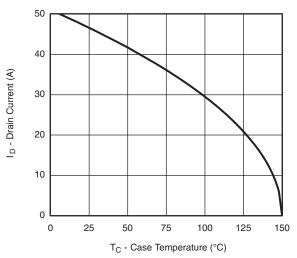


* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

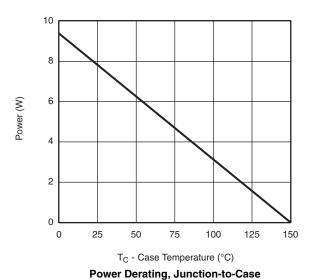
Safe Operating Area, Junction-to-Ambient

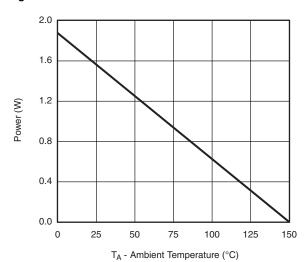


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating*



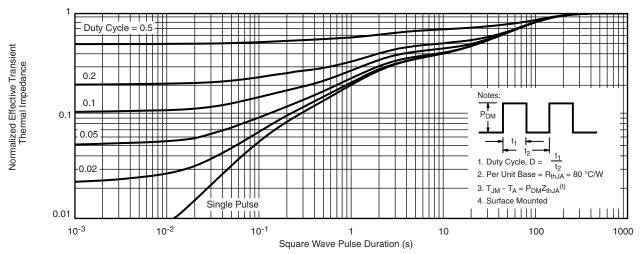


Power Derating, Junction-to-Ambient

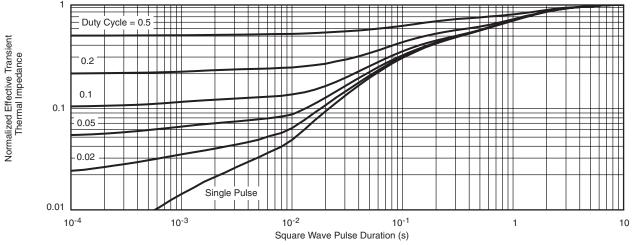
^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INCHES			
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050	0.050 BSC		
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I. 11-Sep-06						

DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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